ESIM PROJECT

DECODER DESIGN AMD SIMULATION

Done by :

Jishnu Saurav Mittapalli

Shaumaya Ojha

Guided By:

Dr.R Maheswari

SCSE, VIT CHENNAI.

ABSTRACT:

In digital electronics, a **binary decoder** is a combinational logic circuit that converts binary information from the n coded inputs to a maximum of 2^n unique outputs. They are used in a wide variety of applications, including data demultiplexing, seven segment displays, and memory address decoding.

OBJECTIVE OF THE DESIGN:

The main aim of this project is to design , implement and simulate a decoder using the esim software .The need for this is to see , verify and learn the outputs of a decoder for given inputs.

Basically the required schematic design is to be created.

EXPECTED OUTCOME:

The expected outcome is the implemented version of a basic decoder.