```
//example 7.10//
clc
//clears the screen//
clear
//clears all variables//
disp('When the external control input is in the logic LOW state,
ABCD=1001. This means output 9 is activated. When the external logic
input is in logic HIGH state, ABCD=1111, this means output line 15 is
activated.')
disp('rest is in diagram')
```