

```
//example 3.9//
clc
//refreshes all variables//
clear
//clears the screen//
disp('Since all the other inputs are permanently tied to logic 1 level,
a logic 0 at the inhibit input would produce a logic 1 at the output
and a logic 1 at the inhibit input would produce a logic 0 at the
output.')
//result//
```