

```
//example 14.4//
clc
//clears the screen//
clear
//clears all existing variables//
disp('The address bit AB10 is low for the first
1024 address inputs (from 00000000000 to
01111111111) and ROM - 1 is selected. For the
remaining 1024 address inputs (from 10000000000
to 11111111111), AB10 bit is HIGH thus enabling
ROM-2.')
```