

Let the name of entity be Circuit_Fig. It has two input ports A and B and one output port Y. The entity declaration for this circuit will be

```
ENTITY Circuit_Fig IS
```

```
PORT(A,B : IN BIT; OUT BIT);
```

```
END Circuit_Fig;
```

From this entity declaration, we observe that although this circuit consists of AND, OR and NOT gates, the circuit itself is an entity and the entity

declaration gives no information about the structure or behaviour of the circuit